

# Encoding Registers

Each register has 4-bit ID

|      |   |
|------|---|
| %eax | 0 |
| %ecx | 1 |
| %edx | 2 |
| %ebx | 3 |

|      |   |
|------|---|
| %esi | 6 |
| %edi | 7 |
| %esp | 4 |
| %ebp | 5 |

- Same encoding as in IA32

Register ID 8 indicates “no register”

- Will use this in our hardware design in multiple places