16-bit logical address

6-bit page #   **10-bit offset**

0	0	0	0	0	1	0	1	1	1	0	1	1	1	1	0

0	000101
1	000110
2	011001

**Process
page table**

0	0	0	1	1	0	0	1	1	1	0	1	1	1	1	0

16-bit physical address

(a) Paging

16-bit logical address

4-bit segment #   **12-bit offset**

0	0	0	1	0	0	1	0	1	1	1	1	0	0	0	0

       Length   **Base**

0	001011101110	0000010000000000
1	011110011110	0010000000100000

Process segment table

$+$

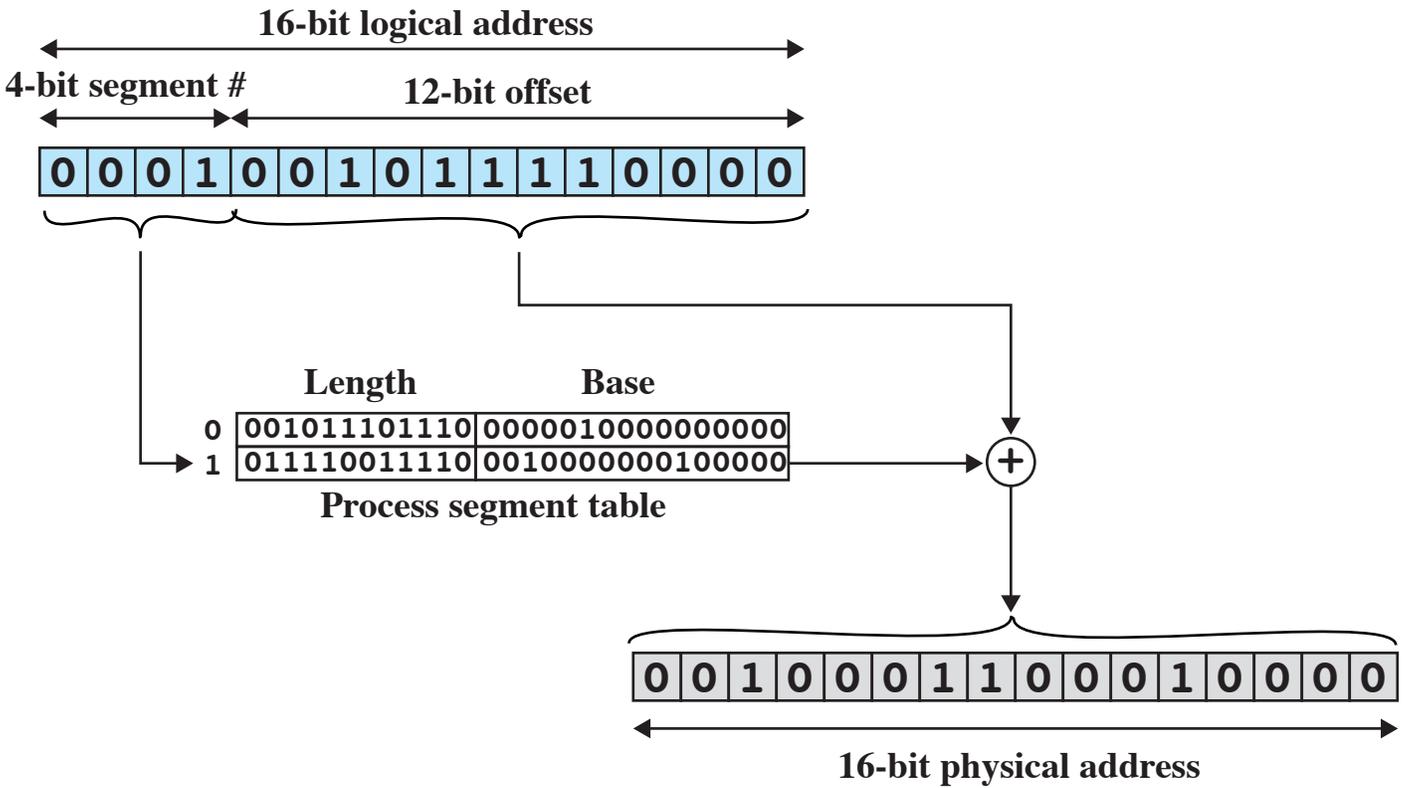| 0	0	1	0	0	0	1	1	0	0	0	1	0	0	0	0

16-bit physical address

(b) Segmentation

Figure 7.12 Examples of Logical-to-Physical Address Translation