Start

Return to
Faulted Instruction

CPU checks the TLB

Page Table
Entry in
TLB?

Yes

No

Access Page Table

Page Fault
Handling Routine

OS Instructs CPU
to Read the Page
from Disk

No

Page
in Main
Memory?

Yes

Update TLB

CPU Activates
I/O Hardware

Page Transferred
from Disk to
Main Memory

CPU Generates
Physical Address

Memory
Full?

Yes

No

Perform Page
Replacement

Page Tables
Updated

**Figure 8.8   Operation of Paging and Translation Lookaside Buffer (TLB) [FURH87]**